

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Leonard Forbes et al.

Examiner: Unknown

Serial No. 10/790,510

Group Art Unit: 2823

Filed: March 1, 2004

Docket No.: 303.689US2

Notice of Allowance Date: August 10, 2005

Title: STATIC PASS TRANSISTOR LOGIC WITH TRANSISTORS WITH MULTIPLE VERTICAL GATES

**COMMUNICATION RE: FEE ADDRESS**

Attn: Mail Stop Issue Fee  
Commissioner for Patents  
P.O.Box 1450  
Alexandria, VA 22313-1450

In response to the "Notice of Allowance and Issue Fee Due" please record the "fee address" under provisions of 37 CFR 1.363 as the following:

Micron Technology, Inc.  
8000 South Federal Way  
P.O. Box 6, MS 525  
Boise, ID 83707-0006

The corresponding Customer Number is **26809**.

Please direct any inquiries to the undersigned attorney at (612) 373-6900.

Respectfully submitted,

Leonard Forbes et al.


By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
P.O. Box 2938  
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Date

10 Nov '05

By

  
Timothy B. Clise  
Reg. No. 40,957  
TBC:CMG:cho

**CERTIFICATE UNDER 37 CFR 1.8:** The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Attn: Mail Stop Issue Fee, Hon. Commissioner for Patents and Trademarks, P.O.Box 1450, Alexandria, VA 22313-1450, on this 10 day of November, 2005.

Eric H. Olson  
Name

  
Signature